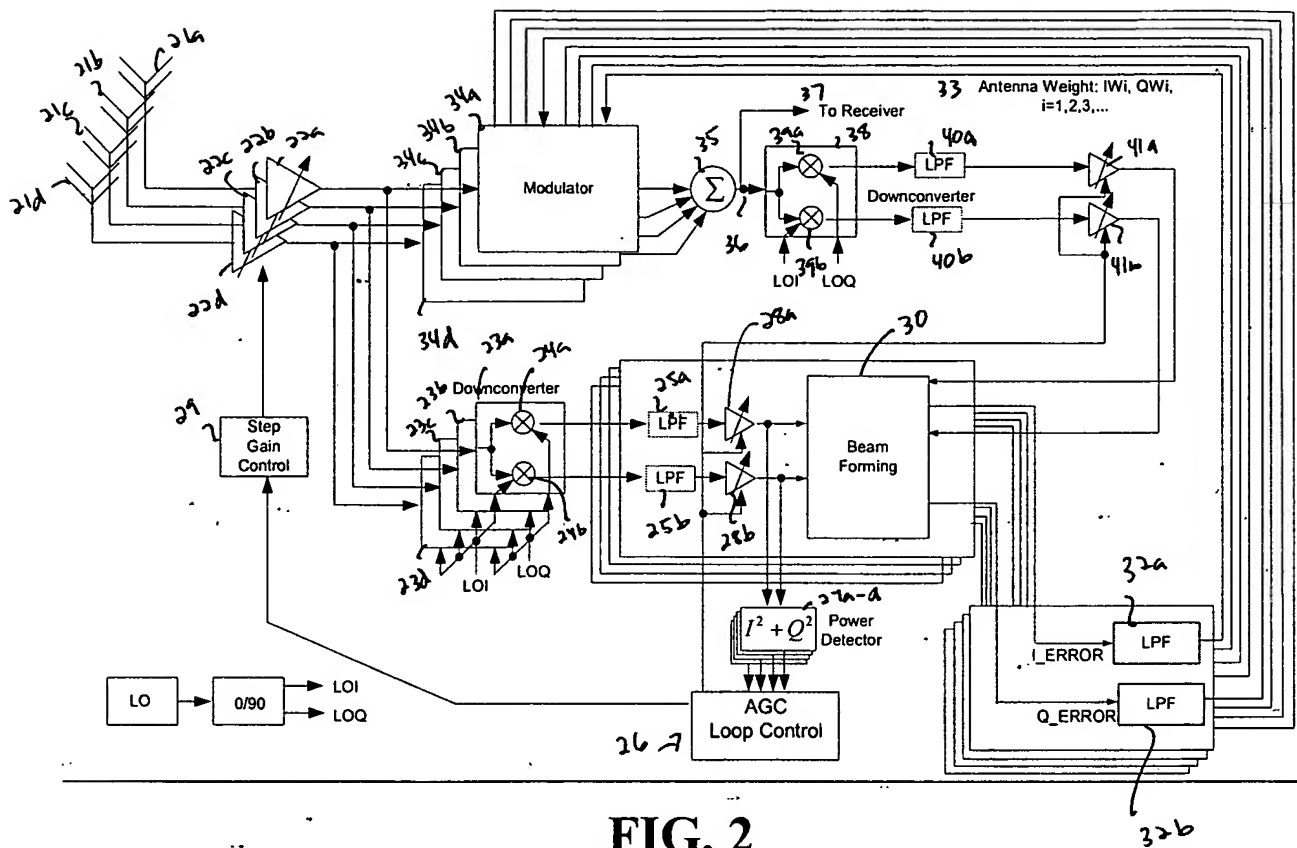
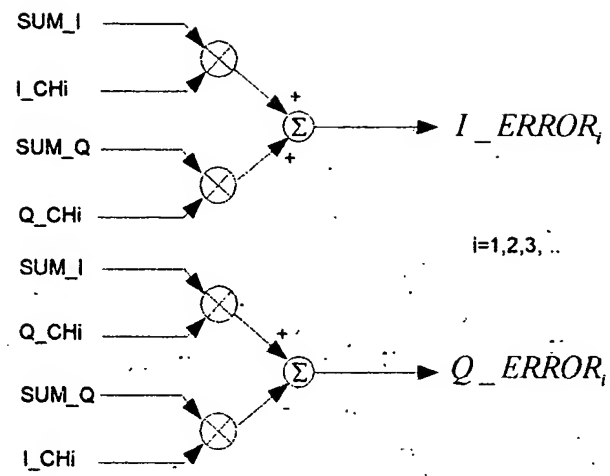
**FIG. 1**



30**FIG. 3**

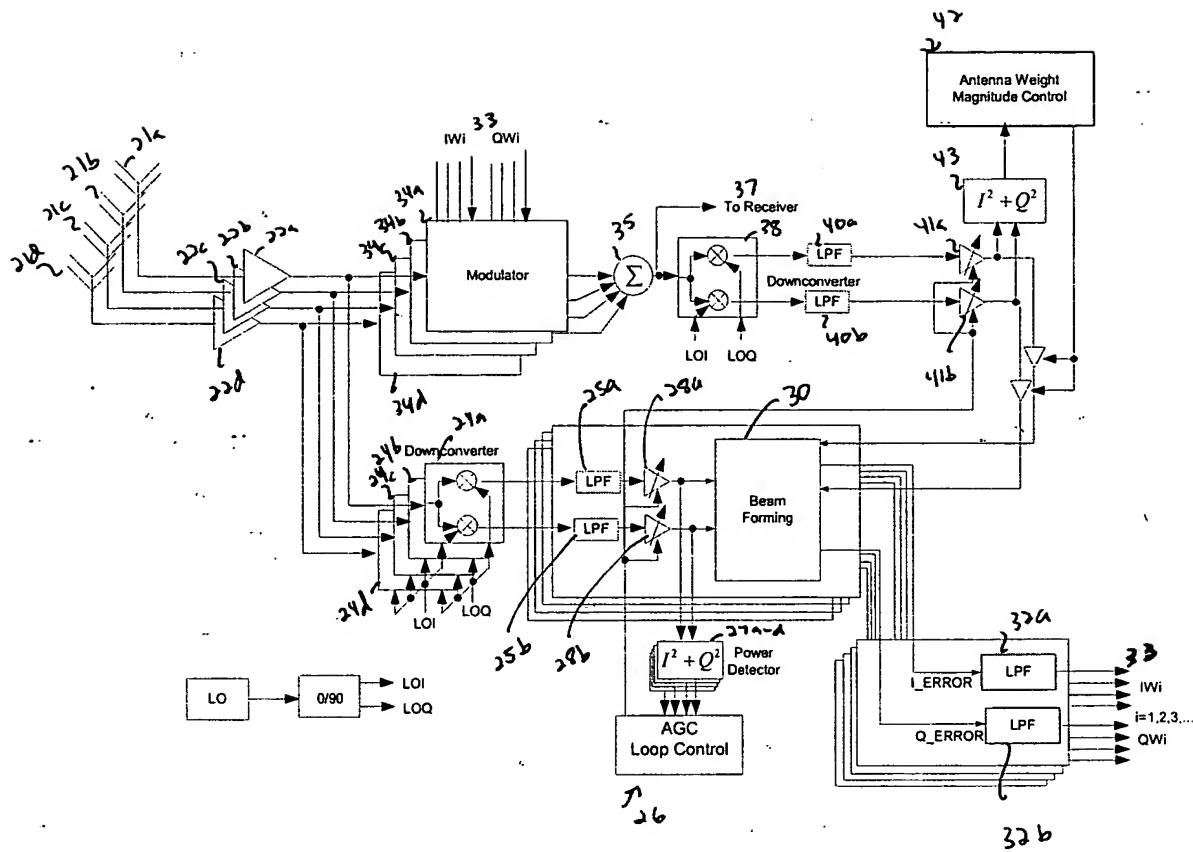


FIG. 4

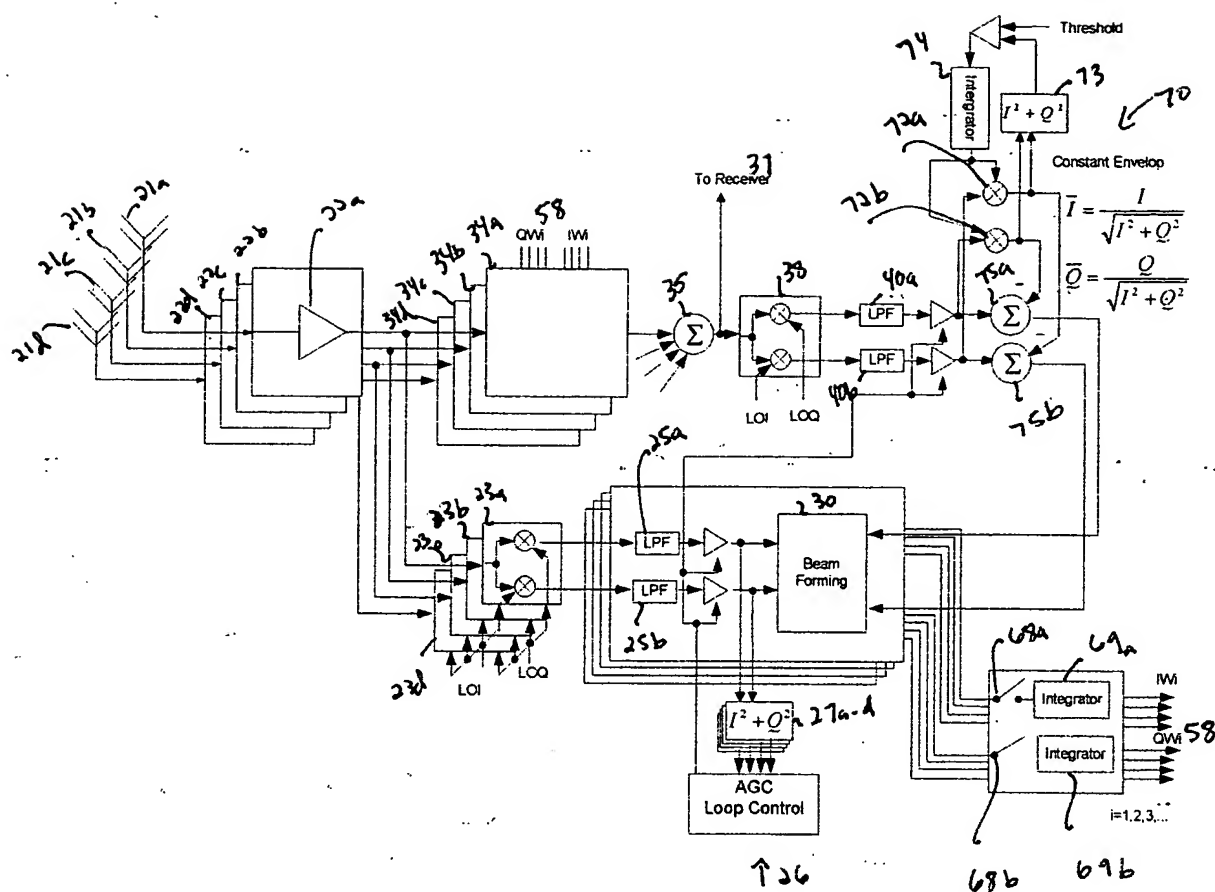


FIG. 7

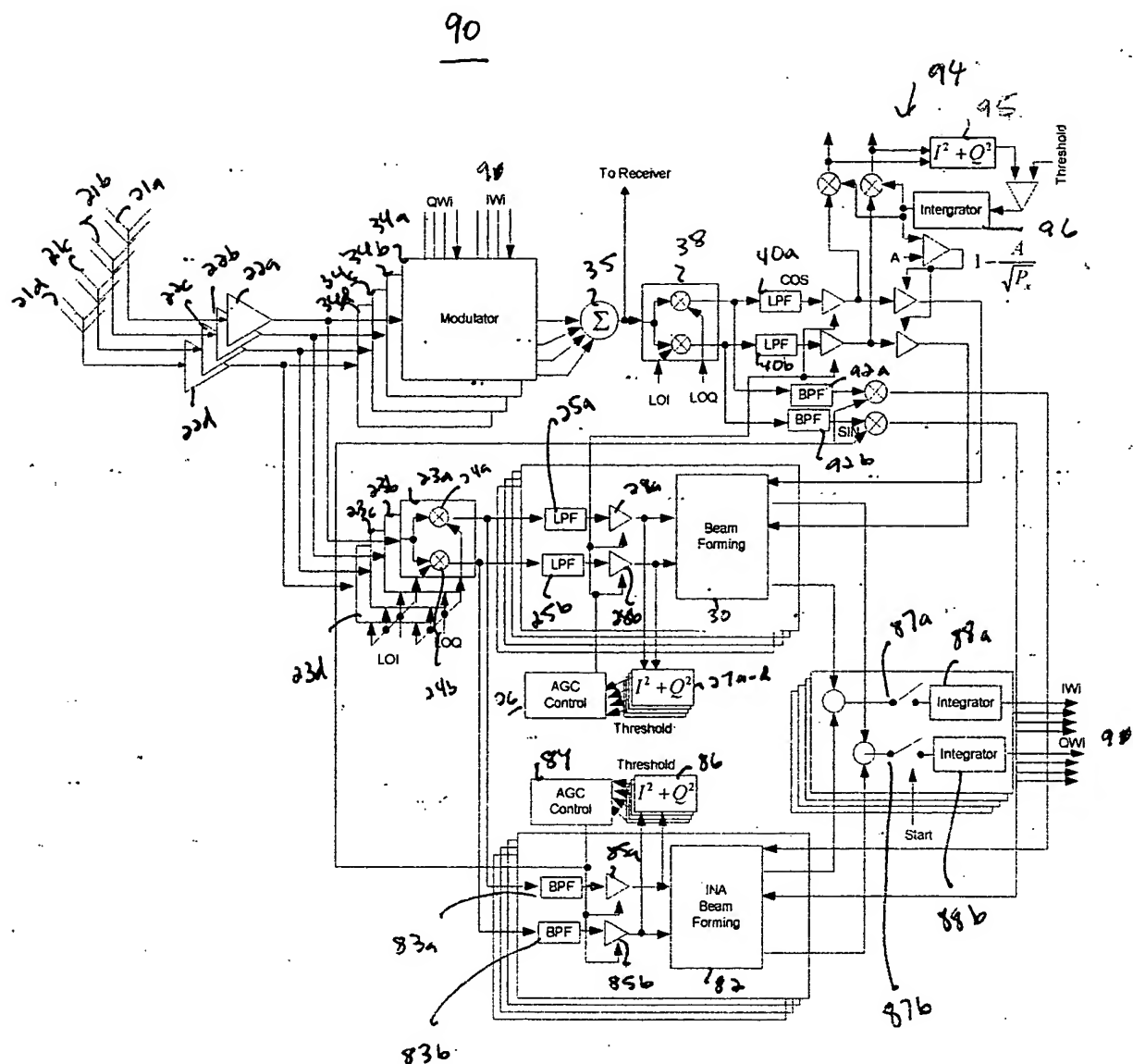
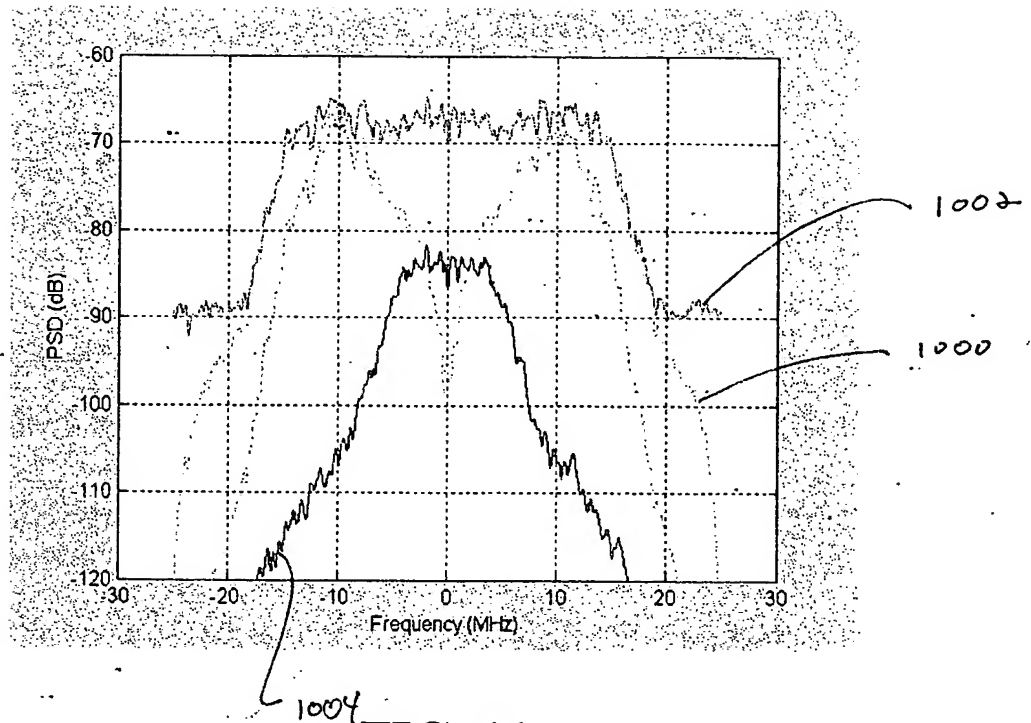
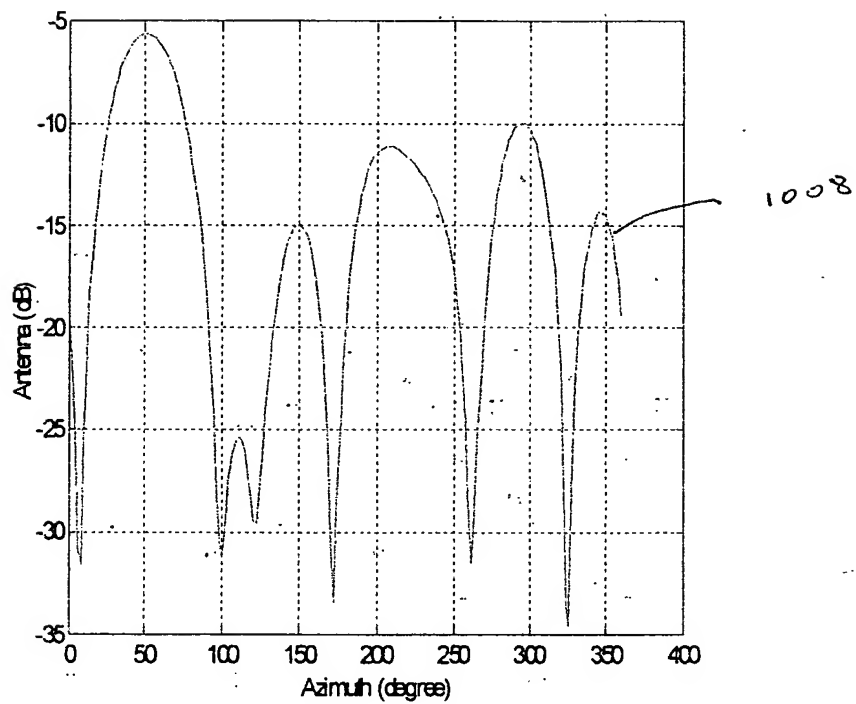
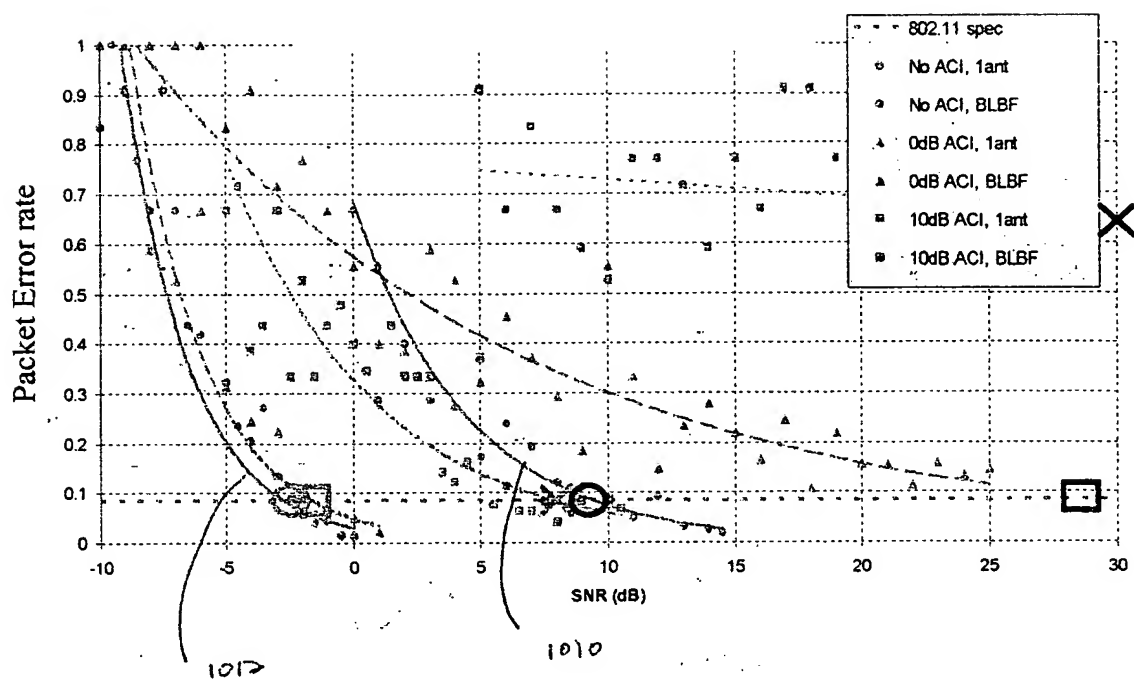


FIG. 9

**FIG. 11**

**FIG. 12**

**FIG. 13**

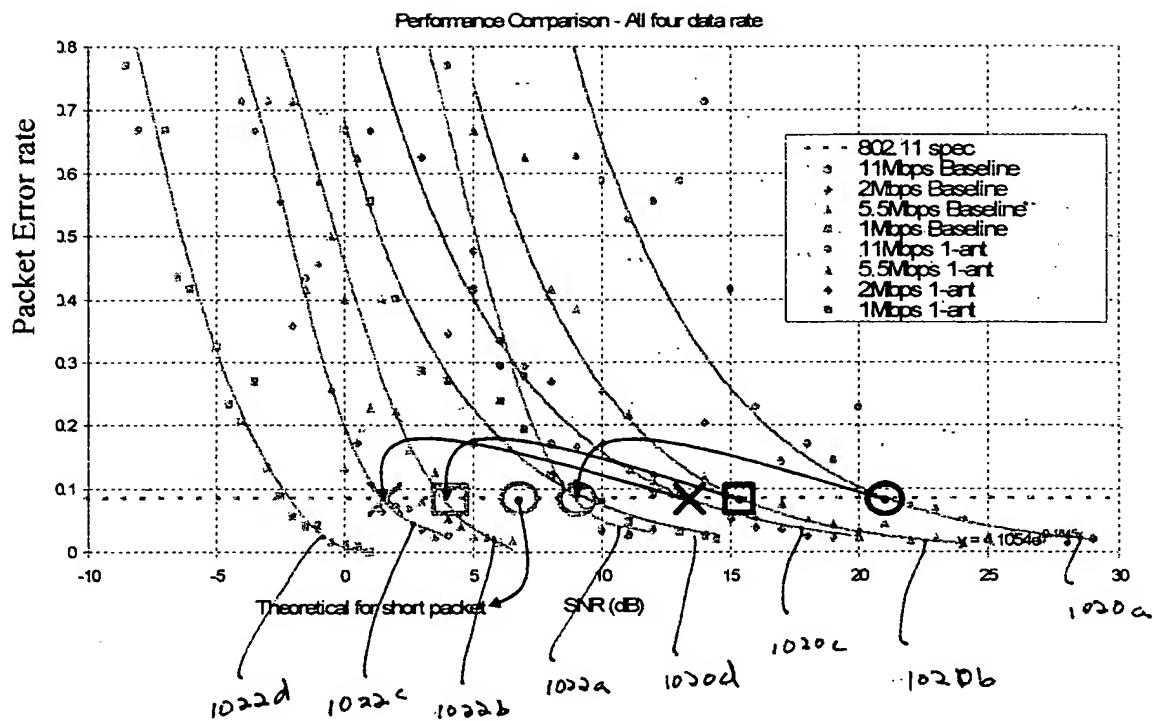


FIG. 14

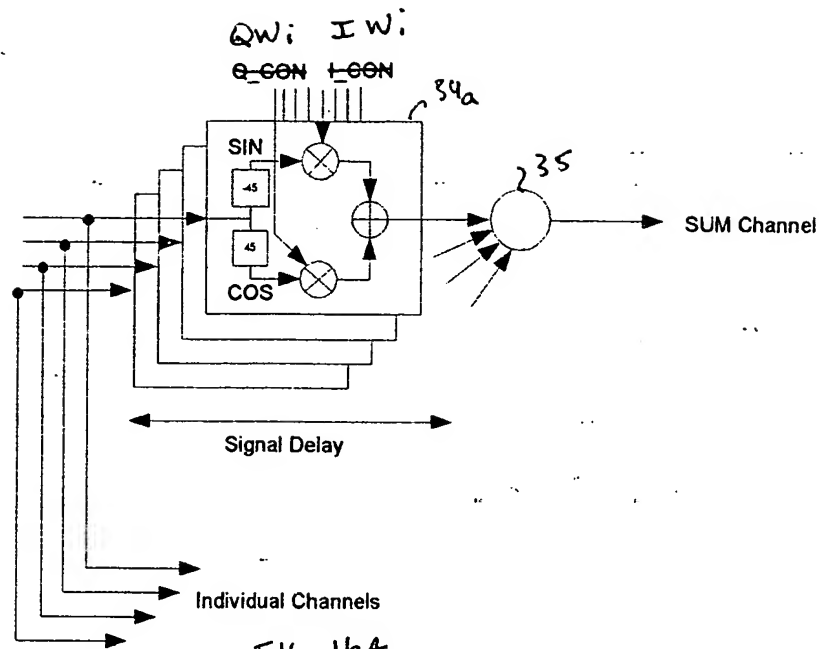
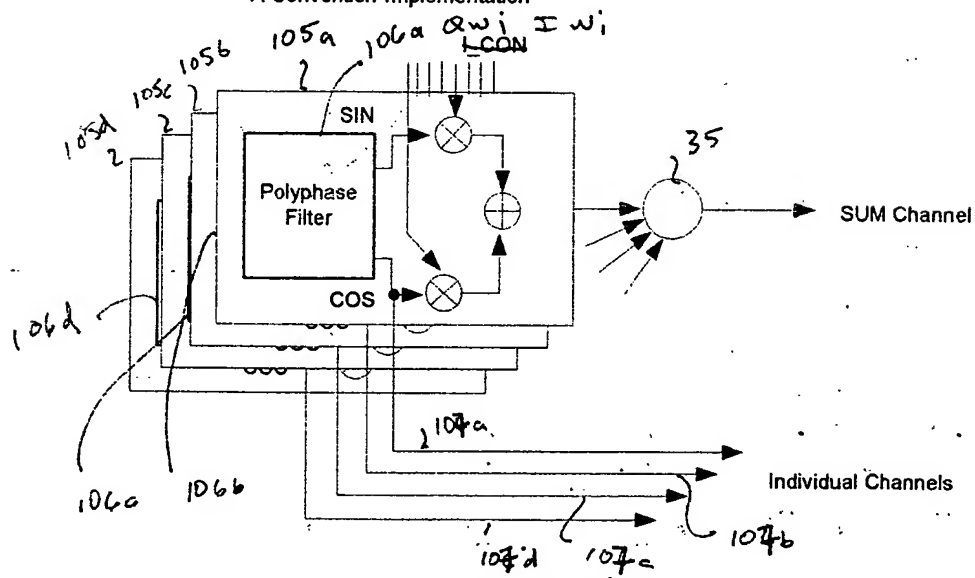


FIG. 16A

A Conventional Implementation



B Implementation with less delay differences

FIG. 16B

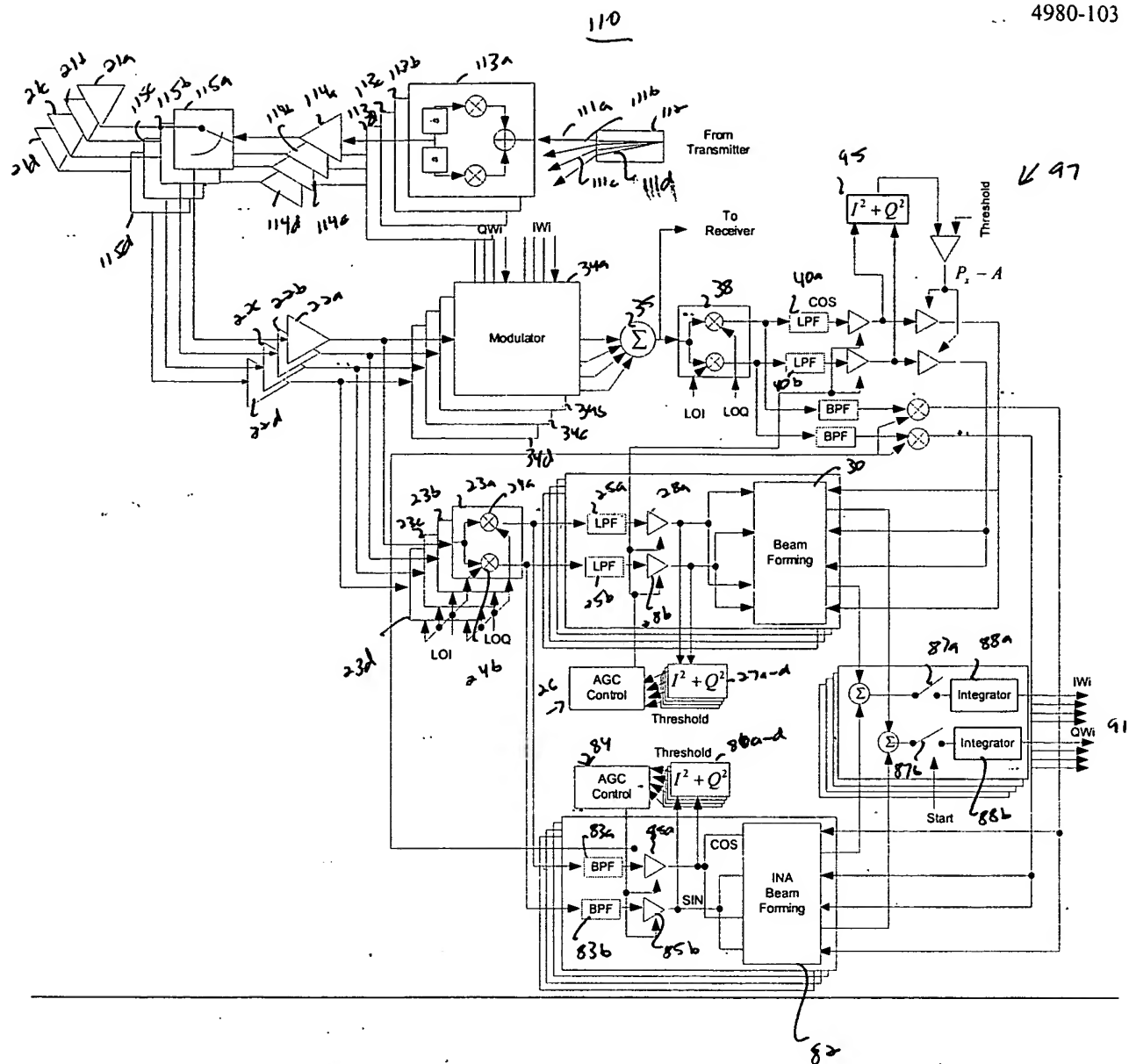
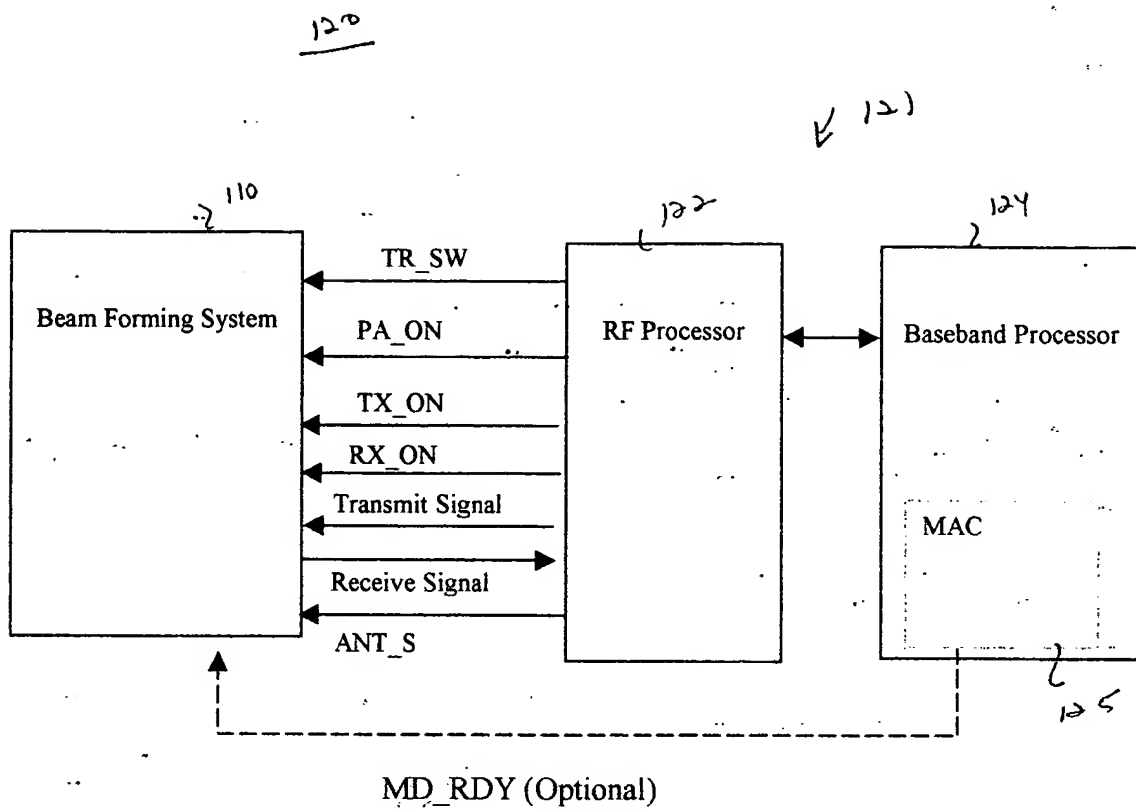


FIG. 17

**FIG. 18**

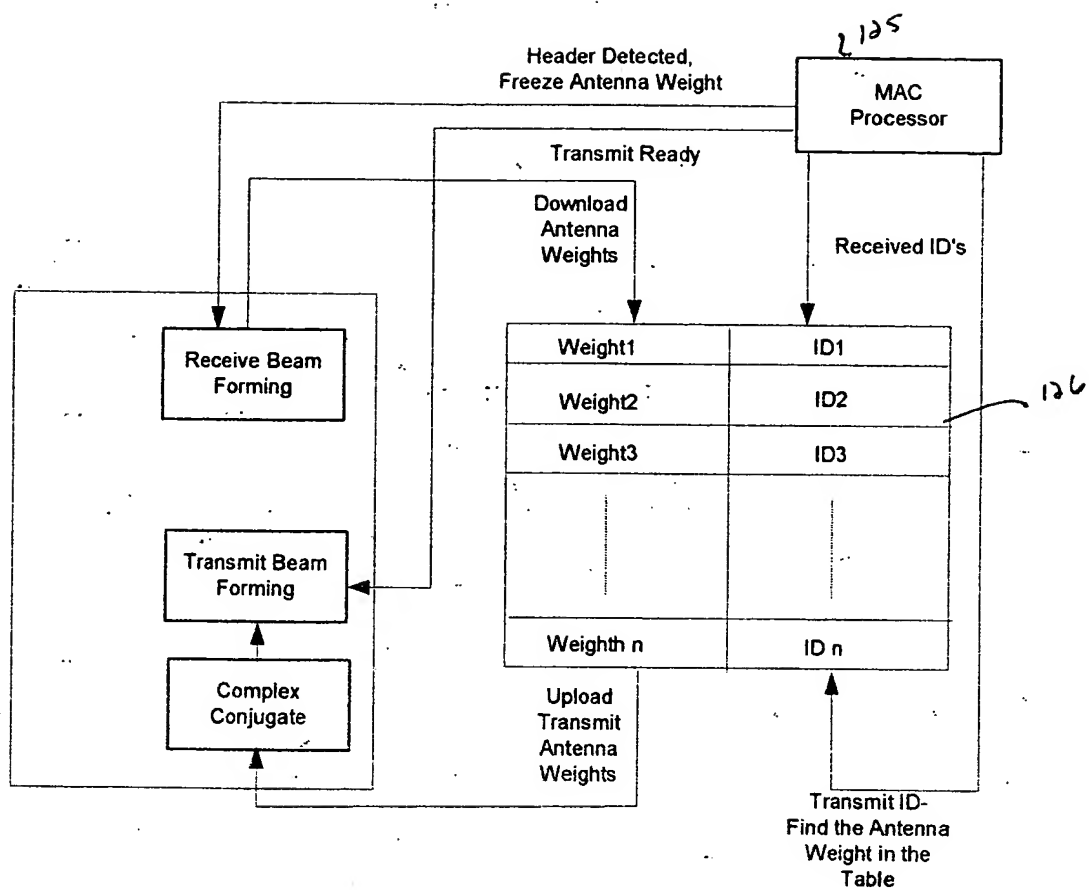


FIG. 19

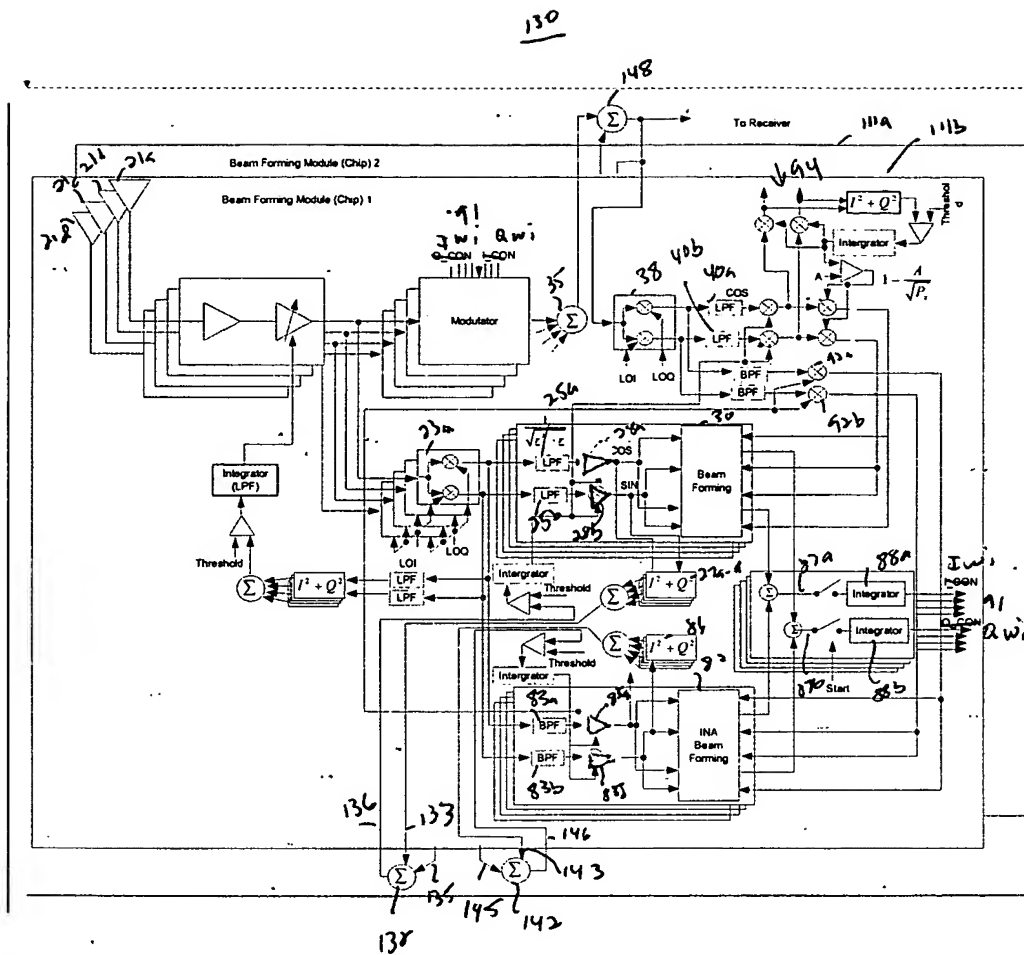


FIG. 20

